



CSC363-Computability Theory@SJTU

Unlimited Register Machine Basic Concepts

Effective Procedures

What is "effective procedure"?

An Example: Consider the function g(n) defined as follows:

 $g(n) = \begin{cases} 1, & \text{if there is a run of exactly } n \text{ consecutive 7's} \\ & \text{in the decimal expansion of } \pi, \\ 0, & \text{otherwise.} \end{cases}$

Question: Is g(n) effective?

 \triangleright The answer is unknown \neq the answer is negative.

Other Examples:

- *Theorem Proving* is in general not effective/algorithmic.
- *Proof Verification* is effective/algorithmic.

CSC363-Computability Theory@SJTU Xiaofeng Gao Unlimited Register Machine CSC363-Computability Theory@SJTU Xiaofeng Gao Unlimited Register Machine Effective Procedures Definition Unlimited Register Machine Computable and Decidable Unlimited Register Machine Computable Function Unlimited Register Machine **Computable Function** When an algorithm or effective procedure is used to calculate the

value of a numerical function then the function in question is effectively calculable (or algorithmically computable, effectively computable, computable).

Examples:

- HCF(x, y) is computable;
- g(n) is non-computable.

Effective Procedures

Basic Concepts

Algorithm

An algorithm is a procedure that consists of a finite set of *instructions* which, given an *input* from some set of possible inputs, enables us to obtain an *output* through a systematic execution of the instructions that terminates in a finite number of steps.

- An Unlimited Register Machine (URM) is an idealized computer.
 - ▷ No limitation in the size of the numbers it can receive as input.
 - ▷ No limitation in the amount of working space available.
 - ▷ Inputs and outpus are restricted to natural numbers. (coding for others)
- From Shepherdson & Sturgis [1963]'s description.
 - ▷ Shepherdson, J. C. & Sturgis, H.E., Computability of Recursive Functions, Journal of Association for Computing Machinery (Journal of ACM), 10, 217-55, 1963.

Unlimited Register Machine Computable and Decidable

Register

A URM has an infinite number of register labeled R_1, R_2, R_3, \ldots

R_1	R_2	R_3	R_4	R_5	R_6	R_7	•••
r_1	r_2	r3	r4	r_5	r_6	r_7	•••

Every register can hold a natural number at any moment.

The registers can be equivalently written as for example

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Instruction

Z(n)

S(n)

 $\overline{T}(m,n)$

J(m, n, q)

Z(n), S(n), T(m, n) are arithmetic instructions.

 $[r_1r_2r_3]_1^3[r_4]_4^4[r_5r_6r_7\ldots]_5^\infty$

or simply

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Type

Zero

Successor

Transfer

Jump

Instruction

$[r_1, r_2, r_3]_1^3 [r_4]_4^4 [r_5, r_6, r_7]_5^7$

Unlimited Register Machine

Replace r_n by 0. $(0 \rightarrow R_n, \text{ or } r_n \coloneqq 0)$

Copy r_m to R_n . $(r_m \to R_n, \text{ or } r_n \coloneqq r_m)$

If $r_m = r_n$, go to the *q*-th instruction;

otherwise go to the next instruction.

Add 1 to r_n . $(r_n + 1 \rightarrow R_n, \text{ or } r_n \coloneqq r_n + 1)$

Instruction

Response of the URM

Unlimited Register Machine Computable and Decidable Notations

Program

A URM also has a program, which is a finite list of instructions.

An instruction is a recognized simple operations (calculation with numbers) to alter the contents of the registers. (I_1, \dots, I_s)

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Configuration and Instructions

Example: The initial registers are:

R_1	R_2	R_3	R_4	R_5	R_6	R_7	•••
9	7	0	0	0	0	0	

The program is:

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 $I_1: J(1,2,6)$ $I_2: S(2)$ $I_3: S(3)$ $I_4: J(1,2,6)$ $I_5: J(1,1,2)$ $I_6: T(3,1)$ Unlimited Register Machine

Configuration and Computation

Configuration:

the contents of the registers + the current instruction number.

Initial configuration, computation, final configuration.

Operation of URM under a program P

- $P = \{I_1, I_2, \cdots, I_s\} \rightarrow \text{URM}$
- URM starts by obeying instruction I_1
- When URM finishes obeying *I_k*, it proceeds to the next instruction in the computation,
 - \triangleright if I_k is not a jump instruction, then the next instruction is I_{k+1} ;
 - ▷ if $I_k = J(m, n, q)$ then next instruction is (1) I_q , if $r_m = r_n$; or (2) I_{k+1} , otherwise.
- Computation stops when the next instruction is I_v , where v > s.
 - \triangleright if k = s, and I_s is an arithmetic instruction;
 - \triangleright if $I_k = J(m, n, q)$, $r_m = r_n$ and q > s;
 - ▷ if $I_k = J(m, n, q)$, $r_m \neq r_n$ and k = s.



Effective Procedures					
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Computable and Decidable					

URM-Computable Func Decidable and Computal

URM-Computable Function

URM-Computable Function

What does it mean that a URM computes a (partial) n-ary function f?

Let *P* be the program of a URM and $a_1, \ldots, a_n, b \in \mathbb{N}$. When computation $P(a_1, \ldots, a_n)$ converges to *b* if $P(a_1, \ldots, a_n) \downarrow$ and $r_1 = b$ in the final configuration. We write $P(a_1, \ldots, a_n) \downarrow b$.

• *P* URM-computes *f* if, for all $a_1, \ldots, a_n, b \in \mathbb{N}$,

 $P(a_1,\ldots,a_n)\downarrow b \operatorname{iff} f(a_1,\ldots,a_n)=b$

- Function *f* is URM-computable if there is a program that URM-computes *f*.
- (We abbreviate "URM-computable" to "computable")



Computable and Decidable

Examples

Construct a URM that computes
$$\dot{x-1} = \begin{cases} x-1, & \text{if } x > 0 \\ 0, & \text{if } x = 0 \end{cases}$$



Computable and Decidable

URM-Computable Function

Function Defined by Program

Given any program *P* and $n \ge 1$, by thinking of the effect of *P* on initial configurations of the form $a_1, \dots, a_n, 0, 0, \dots$, there is a unique *n*-ary function that *P* computes, denoted by $f_P^{(n)}$.

$$f_P^{(n)}(a_1,\ldots,a_n) = \begin{cases} b, & \text{if } P(a_1,\ldots,a_n) \downarrow b, \\ \text{undefined}, & \text{if } P(a_1,\ldots,a_n) \uparrow. \end{cases}$$

Computable and Decidable

Examples



The value of a predicate is either 'true' or 'false'.

The answer of a *decision problem* is either 'yes' or 'no'.

Example: Given two numbers x, y, check whether x is a multiple of y. Input: x, y; Output: 'Yes' or 'No'.

The operation amounts to calculation of the function

$$f(x,y) = \begin{cases} 1, & \text{if } x \text{ is a multiple of } y, \\ 0, & \text{if otherwise.} \end{cases}$$

Thus the property or predicate 'x is a multiple of y' is algorithmically or effectively decidable, or just decidable if function f is computable.

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Decidable Predicate and Decidable Problem

Suppose that $M(x_1, ..., x_n)$ is an *n*-ary predicate of natural numbers. The characteristic function $c_M(\mathbf{x})$, where $\mathbf{x} = x_1, ..., x_n$, is given by

 $f_P^{(n)}(a_1,\ldots,a_n) = \begin{cases} 1, & \text{if } M(\mathbf{x}) \text{ holds,} \\ 0, & \text{if otherwise.} \end{cases}$

The predicate $M(\mathbf{x})$ is decidable if c_M is computable; it is undecidable otherwise.

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URM-Computable Function Decidable and Computable

Computability on other Domains

Suppose *D* is an object domain. A coding of *D* is an explicit and effective injection $\alpha : D \to \mathbb{N}$. We say that an object $d \in D$ is coded by the natural number $\alpha(d)$.

A function $f : D \to D$ extends to a numeric function $f^* : \mathbb{N} \to \mathbb{N}$. We say that f is computable if f^* is computable.

$$f^* = \alpha \circ f \circ \alpha^{-1}$$

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Example				Example (Continue	ed)		

Consider the domain $\mathbb Z.$ An explicit coding is given by the function α where

$$\alpha(n) = \begin{cases} 2n, & \text{if } n \ge 0, \\ -2n-1, & \text{if } n < 0. \end{cases}$$

Then α^{-1} is given by

$$\alpha^{-1}(m) = \begin{cases} \frac{1}{2}m, & \text{if } m \text{ is even,} \\ -\frac{1}{2}(m+1), & \text{if } m \text{ is odd.} \end{cases}$$

Example (Continued)

Consider the function f(x) = x - 1 on \mathbb{Z} , then $f^* : \mathbb{N} \to \mathbb{N}$ is given by

$$f^*(x) = \begin{cases} 1 & \text{if } x = 0 \text{ (i.e. } x = \alpha(0)), \\ x - 2 & \text{if } x > 0 \text{ and } x \text{ is even (i.e. } x = \alpha(n), n > 0), \\ x + 2 & \text{if } x \text{ is odd (i.e. } x = \alpha(n), n < 0). \end{cases}$$

It is a routine exercise to write a program that computes f^* , hence x - 1 is a computable function on \mathbb{Z} .

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Remark

Register Machines are more advanced than Turing Machines.

Register Machine Models can be classified into three groups:

- CM (Counter Machine Model).
- RAM (Random Access Machine Model).
- RASP (Random Access Stored Program Machine Model).

The Unlimited Register Machine Model belongs to the CM class.

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Register Machine Joining Programs Together

Finiteness

Every URM uses only a fixed finite number of registers, no matter how large an input number is.

This is a fine property of Counter Machine Model.

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Sequential Composition	Lemma
Given Programs <i>P</i> and <i>Q</i> , how do we construct the sequential composition <i>P</i> ; <i>Q</i> ? The jump instructions of <i>P</i> and <i>Q</i> must be modified. Standard Form : A program $P = I_1, \ldots, I_s$ is in <i>standard form</i> if, for every jump instruction $J(m, n, q)$ we have $q \le s + 1$.	For any program <i>P</i> there is a program <i>P</i> [*] in standard form such that any computation under <i>P</i> [*] is identical to the corresponding computation under <i>P</i> . In particular, for any a_1, \dots, a_n, b , $P(a_1, \dots, a_n) \downarrow b$ if and only if $P^*(a_1, \dots, a_n) \downarrow b$, and hence $f_P^{(n)} = f_{P^*}^{(n)}$ for every $n > 0$.

Joining Programs Together Notation

Proof

Joining Programs Together

Join/Concatenation

Suppose that $P = I_1, I_2, \cdots, I_s$. Put $P^* = I_1^*, I_2^*, \cdots, I_s^*$ where

if I_k is not a jump instruction, then $I_k^* = I_k$;

if
$$I_k$$
 is not a jump instruction, then $I_k^* = \begin{cases} I_k & \text{if } q \le s+1, \\ J(m, n, s+1) & \text{if } q > s+1. \end{cases}$

Let *P* and *Q* be programs of lengths *s*, *t* respectively, in standard form. The *join* or *concatenation* of P and Q, written PQ or $_{Q}^{P}$, is a program $I_1, I_2, \dots, I_s, I_{s+1}, \dots, I_{s+t}$ where $P = I_1, \dots, I_s$ and the instructions I_{s+1}, \dots, I_{s+t} are the instructions of Q with each jump J(m, n, q)replaced by J(m, n, s + q).

